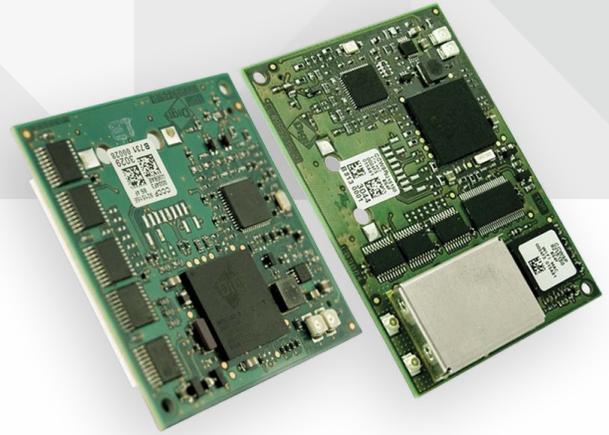




HIGHLY INTEGRATED  
NETWORK-ENABLED  
CORE MODULES



# CONNECTCORE<sup>®</sup> 9P 9215 FAMILY

Compact ARM9-based core modules combine main processing functionality and unique interface flexibility with secure wired and wireless network connectivity

The ConnectCore 9P 9215 family delivers pin-compatible modules utilizing Digi's 32-bit NS9215 processor and secure 802.11a/b/g WLAN technology, offering the industry's only network-enabled modules with true long-term product availability that meet the extended life cycle requirements of embedded product designs.

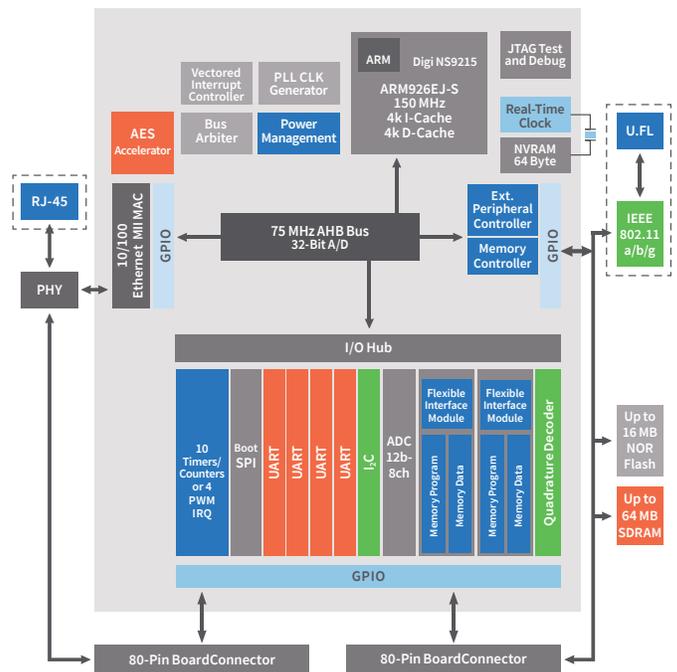
The unique FIMs on the NS9215 processor are two independent 300 MHz DRPIC165X processor cores that allow you to dynamically select application-specific interfaces in software. The growing list of supported interfaces includes UART, SD/SDIO, CAN bus, 1-Wire<sup>®</sup>, USB device low-speed, I<sup>2</sup>S and others.

Cost-effective and easy-to-use Digi JumpStart Kit<sup>®</sup> development solutions allow you to take advantage of the reliable and royalty-free ThreadX<sup>®</sup>-based NET+OS<sup>®</sup> platform or the readily available library of software and strong community of the Linux<sup>®</sup> environment.

## BENEFITS

- Compact, interchangeable 150 MHz ARM9 modules
- Integrated 10/100 Mbit Ethernet interface
- 802.11a/b/g wireless LAN with WPA2/802.11i security
- Rich set of peripheral connectivity options
- 2 programmable interface controllers (FIM)
- Industrial/extended temperature range
- Pre-certified low-emission design
- Digi processor and WLAN technology for true long-term availability
- Seamless migration path to Digi NET+ARM system-on-chip solution

## BLOCK DIAGRAM



## RELATED PRODUCTS



# DIGI JUMPSTART KITS® OVERVIEW

## DIGI JUMPSTART KIT FOR NET+OS

This royalty-free turnkey solution for embedded software development is based on the ThreadX Real-Time Operating System (RTOS), one of the most reliable and field-proven RTOS solutions available. In addition to ThreadX, NET+OS provides the integrated building blocks needed to create product solutions with leading network security using Digi embedded modules and microprocessors.

For professional NET+OS software development, the Eclipse based Digi ESP™ Integrated Development Environment (IDE) for Microsoft Windows with graphical user interface and a high-speed USB 2.0 hardware debugger, is provided out-of-the-box.

- Royalty-free turn-key solution for embedded development
- Built on field-proven and compact ThreadX RTOS
- Fully integrated support for secure, IPv4/IPv6 networking applications
- Professional software development using Windows-based Digi ESP IDE



## DIGI JUMPSTART KIT FOR EMBEDDED LINUX

Built around a standard Linux 2.6 kernel distribution, the Digi JumpStart Kit for Embedded Linux is tailored to the specific needs of embedded Linux development and provides an easy-to-use, complete off-the-shelf embedded development platform. It includes all components that are required to build secure network-enabled products based on the ConnectCore 9P 9215 family.

The kit includes Digi ESP™ for Embedded Linux, a powerful and fully Linux-hosted Integrated Development Environment based on the open Eclipse™ framework. Ideal for new and experienced Linux developers, Digi ESP improves software design productivity by accelerating and greatly simplifying driver and application development through a user-friendly graphical interface.

- Complete Linux embedded Linux development platform
- Royalty-free and with optimized 2.6 kernel and services
- Linux-based Digi ESP IDE for rapid product development
- Full Linux and Digi Board Support Package (BSP) source



SOFTWARE PLATFORM	NET+OS	EMBEDDED LINUX
<b>MODULE</b>	ConnectCore 9P/Wi-9P 9215 w/ 4 MB NOR Flash, 8 MB SDRAM	ConnectCore 9P/Wi-9P 9215 w/ 16 MB NOR Flash, 32 MB SDRAM
<b>DEVELOPMENT BOARD</b>	Ethernet connector, 4 serial ports (1 x RS-232/422/485, 1 x RS-232, 2 x TTL), user/application connectors, I2C/SPI headers, ADC header, screw terminal for access to 8 GPIO signals, 2 user push-buttons, 2 user LEDs, wake-up button, reset button, 802.3af PoE module connector, antenna connectors, battery backup, 9-30VDC power supply, power switch, mounting holes	
<b>CD/DVD</b>	Digi NET+OS CD: NET+OS 7, Digi ESP IDE, BSP source code, sample code, Green Hills MULTI support option, user documentation	Digi Embedded Linux 4 DVD: Digi Embedded Linux, Digi ESP IDE, Linux and platform specific source code, Universal boot loader source code (U-Boot), sample code, documentation
<b>DOCUMENTATION</b>	Quick start guide, Digi ESP tutorial, NET+OS programmer's guide, NET+OS API documentation, Advanced Web Server, hardware reference manual, development board schematics	Quick start guide, Digi Embedded Linux user's guide, hardware reference manual, development board schematics
<b>POWER SUPPLIES AND ACCESSORIES</b>	External wall power supply (110/240VAC) with interchangeable outlet adapters (North America, EU, UK, and Australia), Ethernet cable, serial cable, antennas	
<b>OTHER</b>	Digi JTAG Link USB 2.0 hardware debugger	—

Please refer to the feature specs on our website for detailed information about the specific software platform capabilities.

## SPECIFICATIONS

ConnectCore® 9P 9215

| ConnectCore® Wi-9P 9215

## HARDWARE

PROCESSOR TYPE	32-bit NS9215 processor	
ARM CORE	ARM926EJ-S	
CACHE	4k I-Cache/4k D-Cache	
PROCESSOR SPEED	150 MHz	
MEMORY BASE POPULATION	Up to 16 MB NOR flash; Up to 32 MB SDRAM	
ADDITIONAL POPULATION OPTIONS	RJ-45 Ethernet connector; 2 / 4 / 8 / 16 MB NOR flash; 8 / 16 / 32 MB SDRAM	
FLEXIBLE INTERFACE MODULES (FIMS)	2 x 300 MHz DRPIC165X CPUs with 2k program/192 bytes data RAM	
ON-CHIP 256-BIT AES ACCELERATOR	Yes	
POWER MANAGEMENT MODES	On-the-fly clock scaling; Low-power sleep modes; Configurable scaling/wake-up events (RTC, ADC, EIRQ, UART, etc.)	
PINS/FORM FACTOR	Small-footprint module with 2 x 80-pin board-to-board connectors	
UART	Up to 4 high-speed UARTs; Maximum data rate 1.8432 Mbps	
GPIO	Up to 64 GPIOs; 2 external IRQs	
SPI	Master data rate 33.3 Mbps; Slave data rate 7.5 Mbps	
I2C	v1.0 bus interface; 7-bit and 10-bit address modes	
EXTERNAL MEMORY BUS	17-bit address and 16-bit data bus with 2 external chip select	
ADC	12-bit/8-channel; External reference	
TIMERS/PWM	Up to ten 16-/32-bit timers/counters Modes: internal timer with external terminal count option, external gated timer, event counter Up to 5 PWM functions	
LEDS	2 (general purpose)	
POR CONTROLLER	Yes	
WATCHDOG TIMER (16-BIT)	Yes	
REAL-TIME CLOCK	With 64 bytes of NVRAM and external battery backup	
JTAG INTERFACE	Yes	
QUADRATURE DECODER/COUNTER	Yes	
5V TOLERANT GENERAL PURPOSE AND MEMORY INPUTS	Yes	
DIMENSIONS (L X W X H)	1.97 in (50 mm) x 1.97 in (50 mm) x 6.1 mm (population option w/o RJ-45)	2.75 in (70 mm) x 1.97 in (50 mm) x 8.6 mm (population option w/o RJ 45)
NETWORK INTERFACE - WIRED		
STANDARD	IEEE 802.3	
PHYSICAL LAYER	10/100Base-T	
DATA RATE	10/100 Mbps (auto-sensing)	
MODE	Full or half duplex (auto-sensing)	
NETWORK INTERFACE - WIRELESS LAN		
STANDARD	IEEE 802.11a/b/g	
SECURITY	WEP/WPA/WPA2/802.11i	
FREQUENCY	2.4/5 GHz	
DATA RATE	Up to 54 Mbps with automatic rate fallback	
MODULATION	DBPSK (1 Mbps), DQPSK (2 Mbps), CCK (11, 5.5 Mbps), BPSK (6, 9 Mbps), QPSK (12, 18 Mbps), 16-QAM (24, 36 Mbps), 64-QAM (48, 54 Mbps)	
TYPICAL TRANSMIT POWER	18 dBm @ 1 Mbps (802.11b); 12 dBm @ 54 Mbps (802.11g)	
RECEIVE SENSITIVITY	-72 dBm @ 54 Mbps (802.11a/g)	
CONNECTORS	2 x U.FL	

