

30V Nch+Nch Power MOSFET

V _{DSS}	30V
R _{DS(on)} (Max.)	5.0mΩ
I _D	14A
P _D	3W

Features

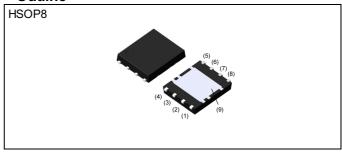
- 1) Low on resistance.
- 2) Pb-free lead plating; RoHS compliant.
- 3) Halogen Free.

Application

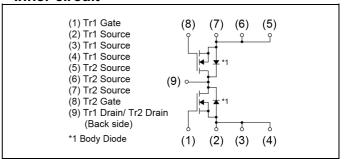
Load Switch

LiB charging and discharging switch

Outline



•Inner circuit



Packaging specifications

	Packing	Embossed Tape			
	Reel size (mm)	330			
Туре	Tape width (mm)	12			
	Basic ordering unit (pcs)	2500			
	Taping code	TB			
	Marking	HP8KA1			

● Absolute maximum ratings (T_a = 25°C) < It is the same ratings for the Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	30	V
Continuous drain current	I _D *1	14	Α
Pulsed drain current	I _{D,pulse} *2	28	Α
Gate - Source voltage	V_{GSS}	±20	V
Power dissipation	P _D *3	3	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Currelle e l	Values			l leit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R _{thJA} *3	-	41	ı	°C/W

ullet Electrical characteristics (T_a = 25°C) <It is the same characteristics for the Tr1 and Tr2>

Darameter	Symbol	Conditions			Values		
raiametei	Parameter Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to	-	21	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V	-	-	1	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	-	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	V _{DS} = 10V, I _D = 10mA	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I _D = 1mA referenced to	-	-3	-	mV/°C	
Static drain - source	D *4	V _{GS} = 10V, I _D = 14A	ı	3.5	5.0	m0	
on - state resistance	R _{DS(on)} *4	V _{GS} = 4.5V, I _D = 14A	-	5.0	7.0	mΩ	
Transconductance	9 _{fs} *4	$V_{DS} = 5V, I_{D} = 14A$	14	-	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} Mounted on 40mm×40mm Cu BOARD

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C) < It is the same characteristics for the Tr1 and Tr2>

Parameter	Sumb of	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Input capacitance	C _{iss}	V _{GS} = 0V	1	2550	1		
Output capacitance	C _{oss}	V _{DS} = 15V	-	330	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	270	-		
Turn - on delay time	t _{d(on)} *4	V _{DD} ≈ 15V,V _{GS} = 10V	-	25	-		
Rise time	t _r *4	I _D = 7A	-	30	-	no	
Turn - off delay time	t _{d(off)} *4	$R_L = 2.1\Omega$	-	85	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	40	ı		

• Gate charge characteristics (T_a = 25°C) < It is the same characteristics for the Tr1 and Tr2>

Darameter	Cymahal	Conditions	Values			l lait
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg*4		-	24	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \approx 15V, I_{D} = 14A$ $V_{GS} = 4.5V$	-	7.5	-	nC
Gate - Drain charge	Q _{gd} *4	VG3 1.0 V	-	9.0	-	

● Body diode electirical characteristics (Source-Drain) (T_a = 25°C)

<It is the same characteristics for the Tr1 and Tr2>

Darameter	Symbol	Conditions	litions		Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Body diode continuous forward current	I _S *1	T = 25°0	-	-	2.5	_	
Body diode pulse current	l _{SP} *2	T _a = 25°C	-	-	28	Α	
Forward voltage	V _{SD} *4	$V_{GS} = 0V, I_{S} = 2.5A$	-	-	1.2	V	

Drain Current : I_D [A]

Fig.1 Power Dissipation Derating Curve

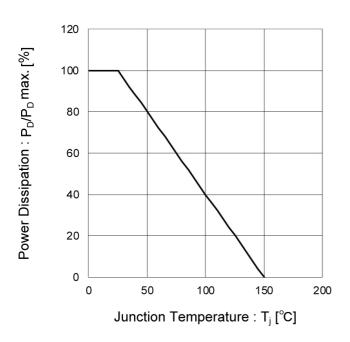
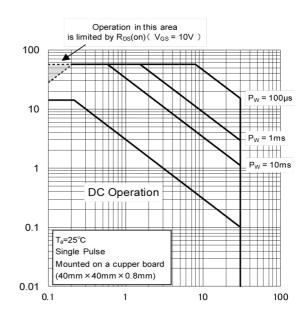


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage: V_{DS}[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

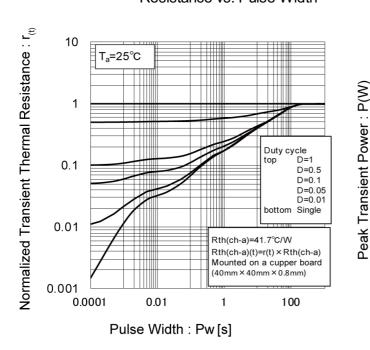
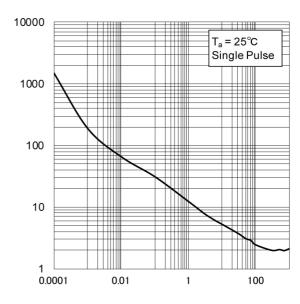


Fig.4 Single Pulse Maximum Power dissipation



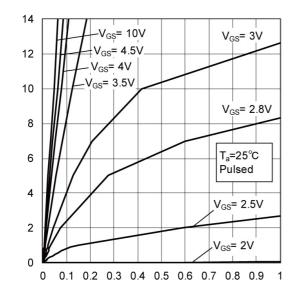
Pulse Width: Pw[s]

Drain Current : I_D [A]

• Electrical characteristics curves < It is the same characteristics for the Tr1 and Tr2>

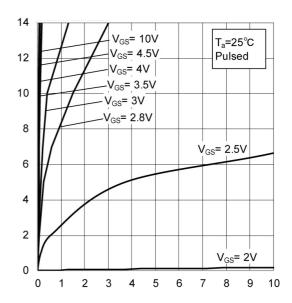
Drain Current : I_D [A]

Fig.5 Typical Output Characteristics(I)



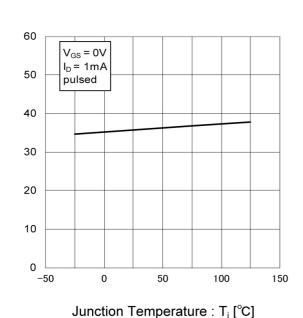
Drain - Source Voltage : $V_{DS}[V]$

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs. Junction Temperature



Drain-Source Breakdown Voltage : $V_{(BR)DSS}$ [V]

Gate Threshold Voltage: V_{GS(th)} [V]

Fig.8 Typical Transfer Characteristics

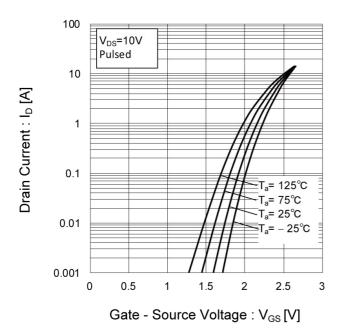
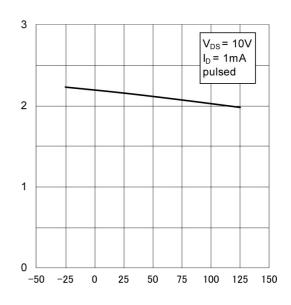


Fig.9 Gate Threshold Voltage vs. Junction Temperature



Junction Temperature : T_j [°C]

Fig.10 Tranceconductance vs. Drain Current

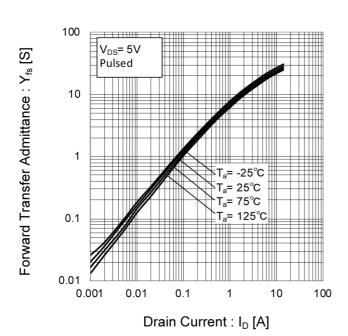


Fig.11 Drain Current Derating Curve

Drain Current Dissipation

100

100

80

80

60

-25

0

25

50

75

100

125

150

Junction Temperature: T_j [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

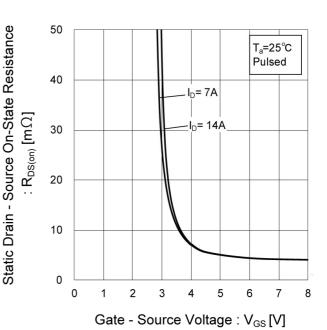
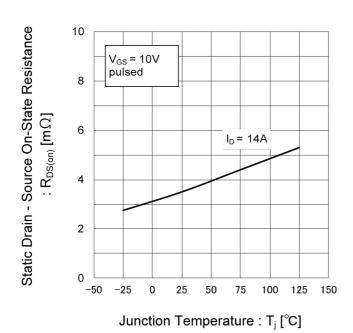


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



Static Drain - Source On-State Resistance

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

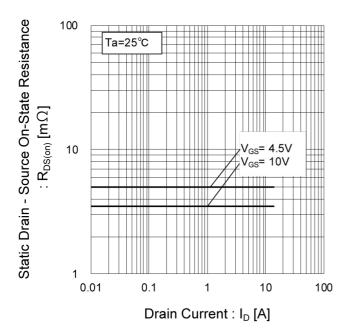


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

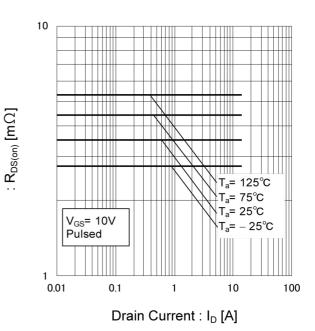
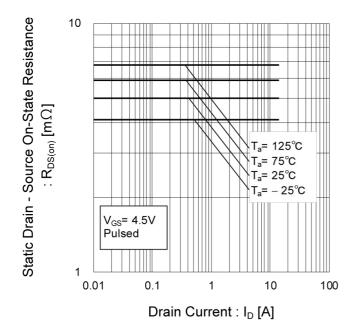


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)



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Fig.17 Typical Capacitance vs. Drain - Source Voltage

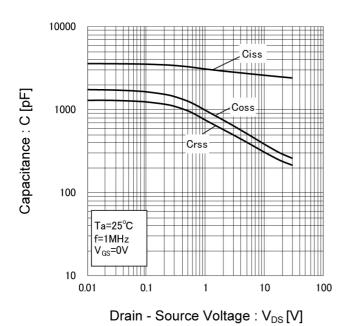


Fig.18 Switching Characteristics

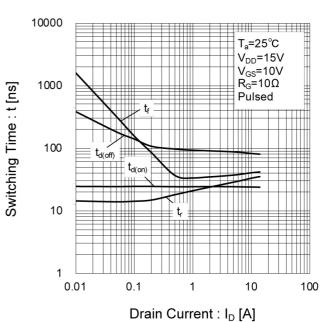


Fig.19 Dynamic Input Characteristics

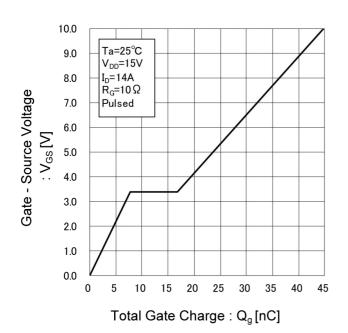
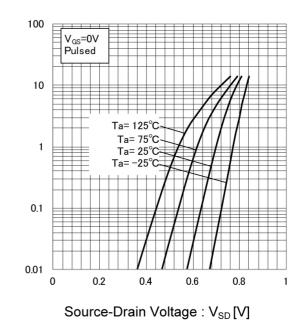


Fig.20 Source Current vs. Source Drain Voltage



Source Current : Is [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

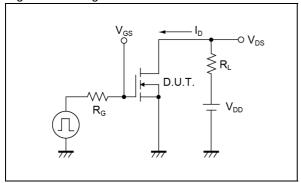


Fig.2-1 Gate Charge Measurement Circuit

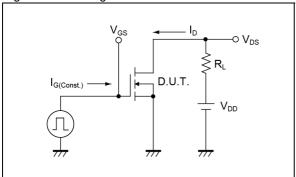


Fig.1-2 Switching Waveforms

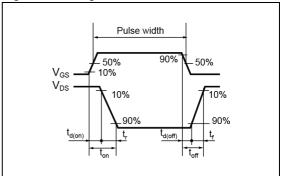
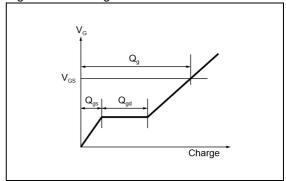
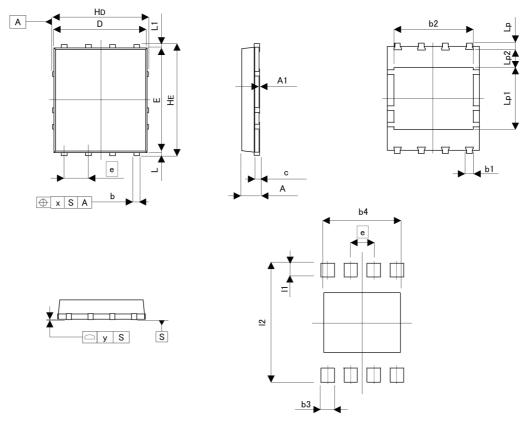


Fig.2-2 Gate Charge Waveform



Dimensions

HSOP8 (Drain common)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.24	0.42	0.009	0.017
b1	0.22	0.52	0.009	0.020
b2	4.00	4.40	0.157	0.173
С	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.60	5.80	0.220	0.228
е	1.:	27	0.050	
HD	4.90	5.10	0.193	0.201
HE	5.90	6.10	0.232	0.240
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.27	0.47	0.011	0.019
Lp1	3.12	3.52	0.123	0.139
Lp2	0.	97	0.0	38
Х	-	0.10	-	0.004
у	-	0.10	-	0.004

DIM	MILIMETERS		INCHES		
DIIVI	MIN	MAX	MIN	MAX	
b3	1-	0.62	-	0.024	
b4	-	4.40	1	0.173	
11	-	0.57	ı	0.022	
12	-	6.10	1	0.240	

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	ОГАСОШ	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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