RoHS EARTH FRIENDLY

GaAs pHEMT MMIC 2 WATT POWER AMPLIFIER, 27.3 - 33.5 GHz

Typical Applications

The HMC906 is ideal for:

- Point-to-Point Radios
- Point-to-Multi-Point Radios
- VSAT
- Military & Space

Features

Saturated Output Power: +34 dBm @ 22% PAE

High Output IP3: +43 dBm

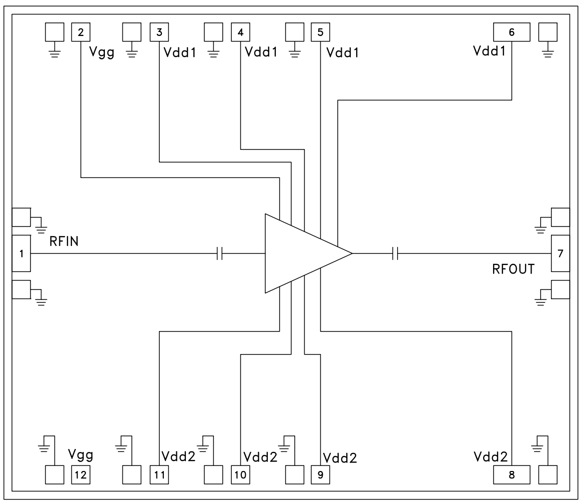
High Gain: 23 dB

DC Supply: +6V @ 1200 mA

No External Matching Required

Die Size: 3.18 x 2.73 x 0.1 mm

Functional Diagram



General Description

The HMC906 is a four stage GaAs pHEMT MMIC 2 Watt Power Amplifier which operates between 27.3 and 33.5 GHz. The HMC906 provides 23 dB of gain, and +34 dBm of saturated output power and 22% PAE from a +6V supply. The RF I/Os are DC blocked and matched to 50 Ohms for ease of integration into Multi-Chip-Modules (MCMs). All data is taken with the chip in a 50 Ohm test fixture connected via two 0.025 mm (1 mil) diameter wire bonds of length 0.31 mm (12 mils).

Electrical Specifications, T_A = +25° C, Vdd1 = Vdd2 = +6 V, Idd = 1200 mA [1]

Parameter	Min.	Typ.	Max.	Min.	Typ.	Max.	Units
Frequency Range		27.3 - 31.5			31.5 - 33.5		GHz
Gain	20	23		20	23		dB
Gain Variation Over Temperature		0.022			0.026		dB/ °C
Input Return Loss	10	14		10	14		dB
Output Return Loss	8	12		10	12		dB
Output Power for 1 dB Compression (P1dB)	31	33		30.5	32.5		dBm
Saturated Output Power (Psat)		34			33.5		dBm
Output Third Order Intercept (IP3)[2]	40	43		39	42		dBm
Total Supply Current (Idd)		1200			1200		mA

[1] Adjust Vgg (pad 2 or 12) between -2 to 0V to achieve Idd = 1200 mA typical.

[2] Measurement taken at +6V @ 1200 mA, Pout / Tone = +23 dBm

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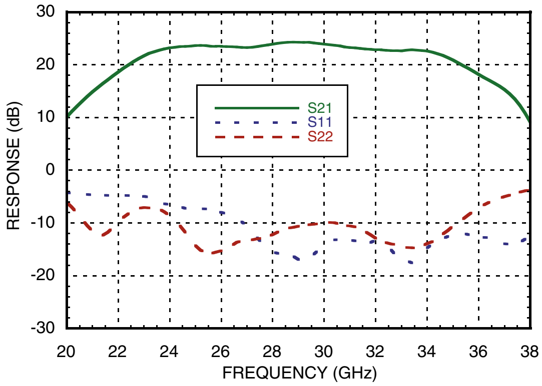
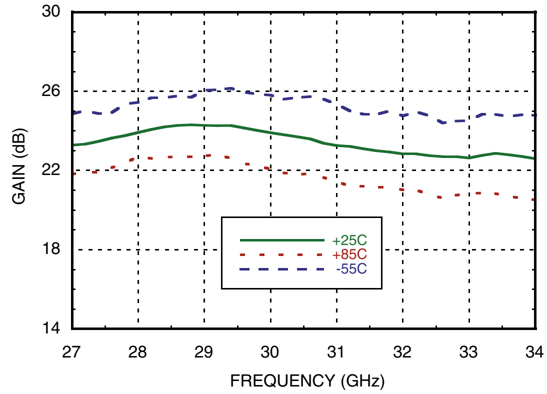
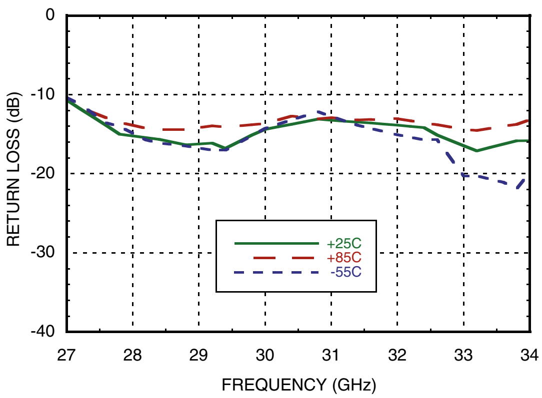
Broadband Gain & Return Loss vs. Frequency



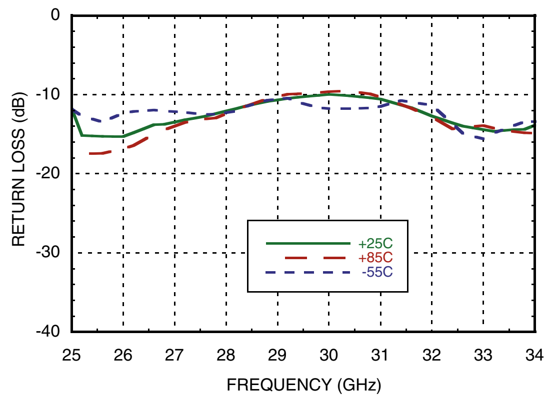
Gain vs. Temperature



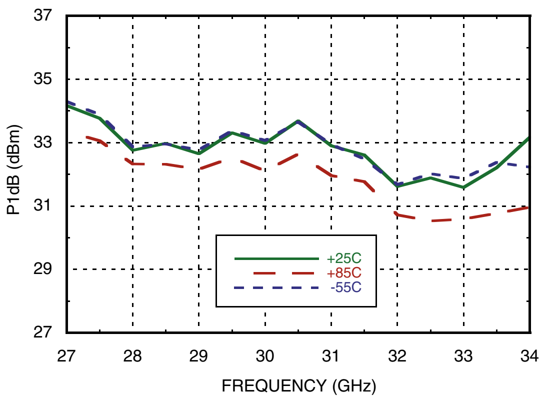
Input Return Loss vs. Temperature



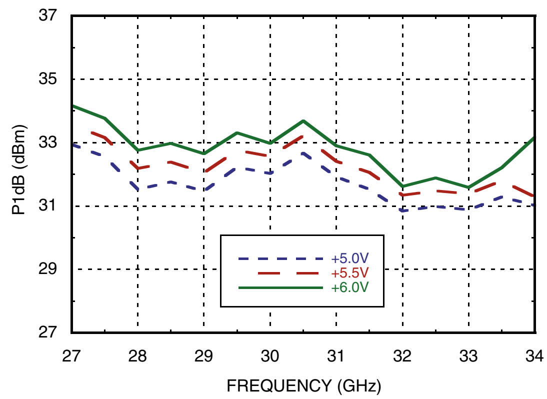
Output Return Loss vs. Temperature



P1dB vs. Temperature



P1dB vs. Supply Voltage



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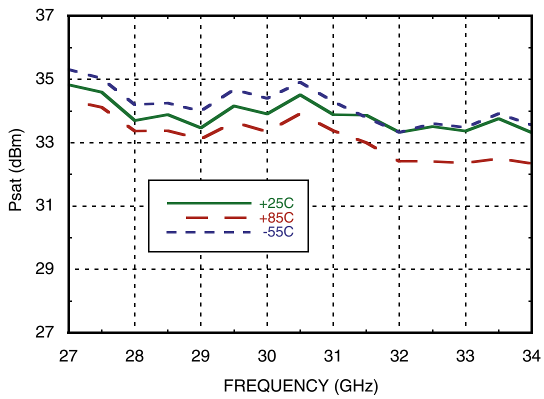
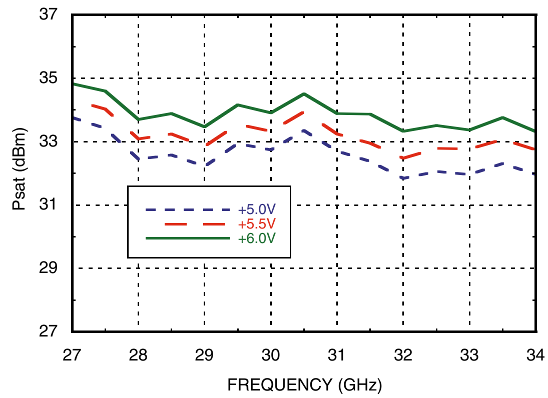
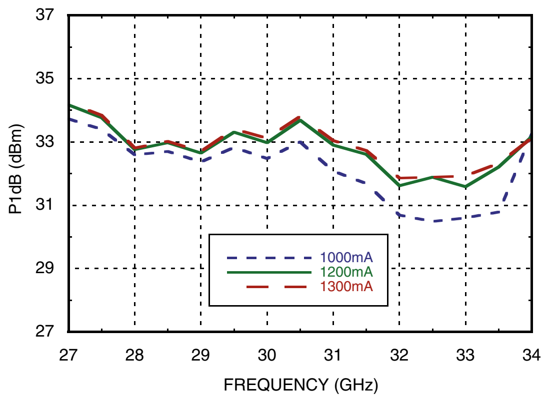
Psat vs. Temperature



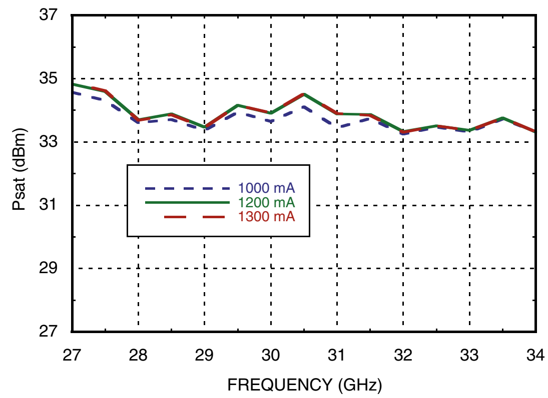
Psat vs. Supply Voltage



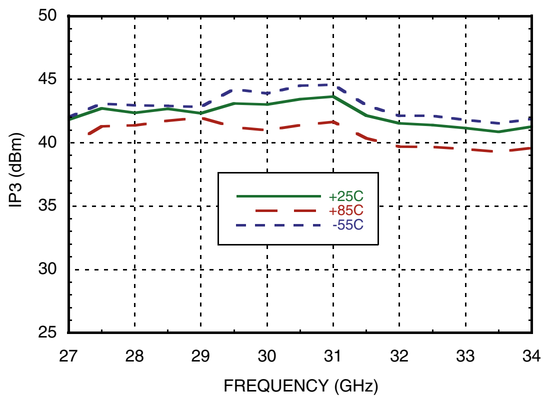
P1dB vs. Supply Current (Idd)



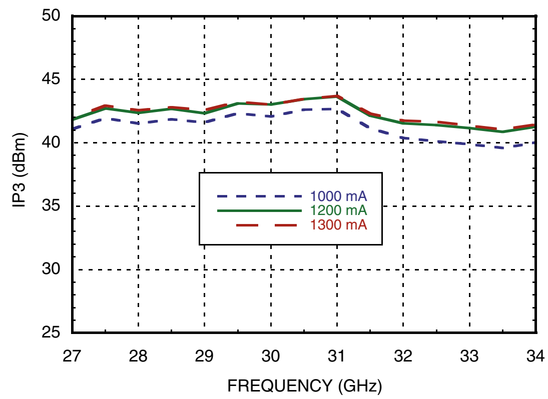
Psat vs. Supply Current (Idd)



Output IP3 vs. Temperature, Pout/Tone = +23 dBm



Output IP3 vs. Supply Current, Pout/Tone = +23 dBm



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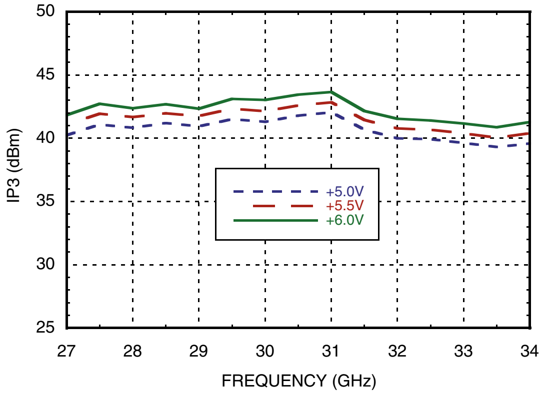
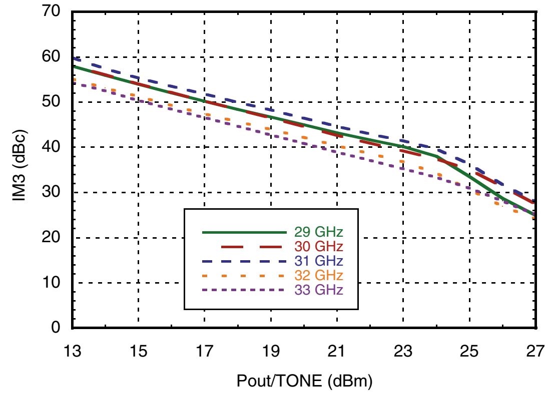
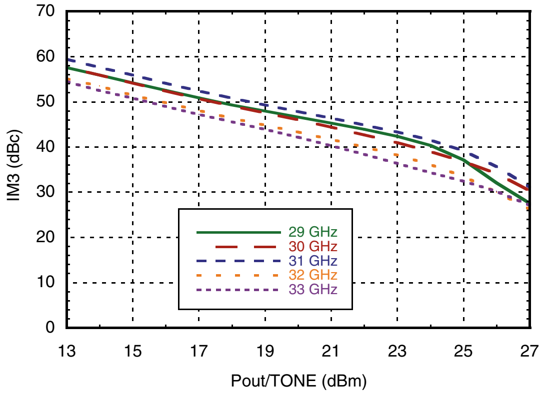
Output IP3 vs. Supply Voltage, Pout/Tone = +23 dBm



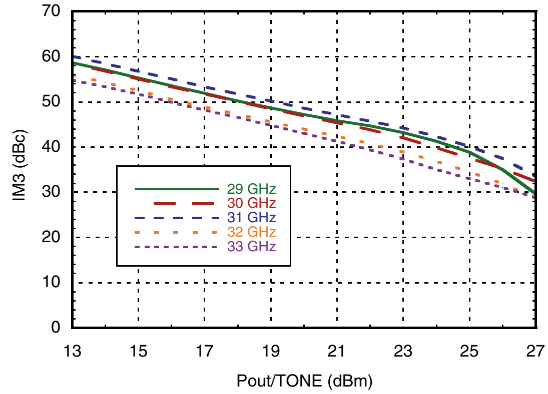
Output IM3 @ Vdd = +5V



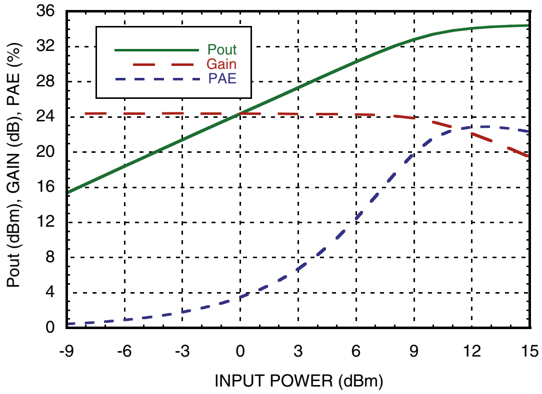
Output IM3 @ Vdd = +5.5V



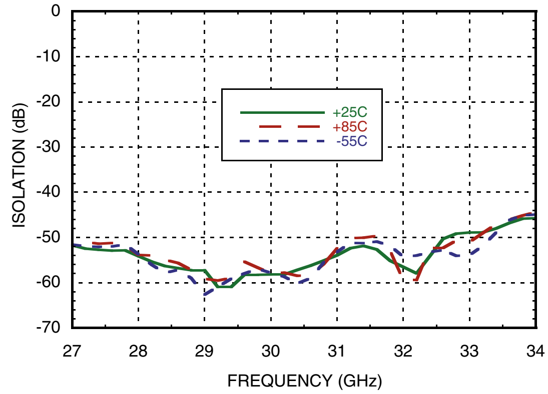
Output IM3 @ Vdd = +6V



Power Compression @ 29.5 GHz



Reverse Isolation vs. Temperature



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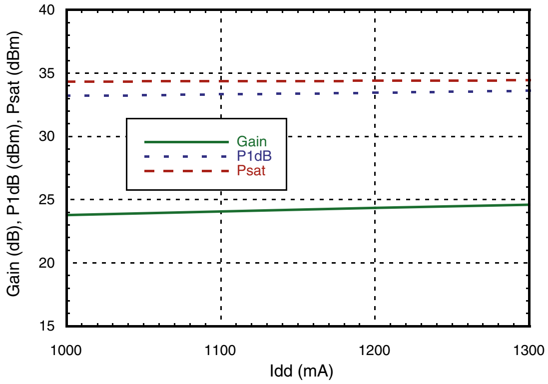
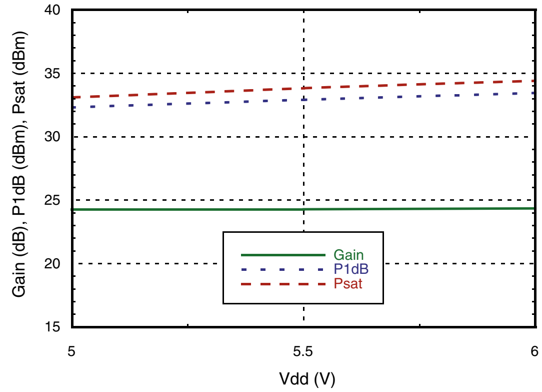
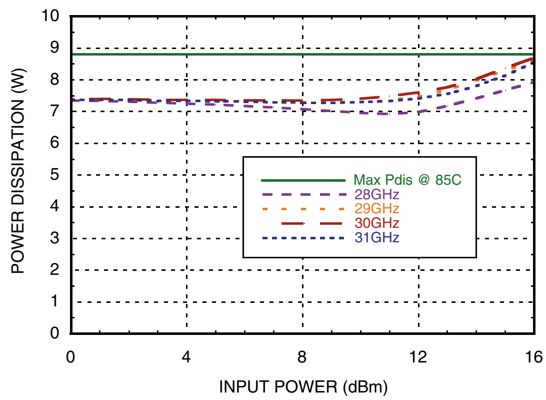
Gain & Power vs. Supply Current @ 29.5 GHz



Gain & Power vs. Supply Voltage @ 29.5 GHz



Power Dissipation



Absolute Maximum Ratings

Drain Bias Voltage (Vd)	+7V
RF Input Power (RFIN)	+20 dBm
Channel Temperature	150 °C
Continuous Pdiss (T= 85 °C) (derate 135 mW/°C above 85 °C)	8.8 W
Thermal Resistance (channel to die bottom)	7.4 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-55 to +85 °C

Typical Supply Current vs. Vdd

Vdd (V)	Idd (mA)
+5.0	1200
+5.5	1200
+6.0	1200

Note: Amplifier will operate over full voltage ranges shown above. Vgg adjusted to achieve Idd = 1200 mA at +6.0V

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

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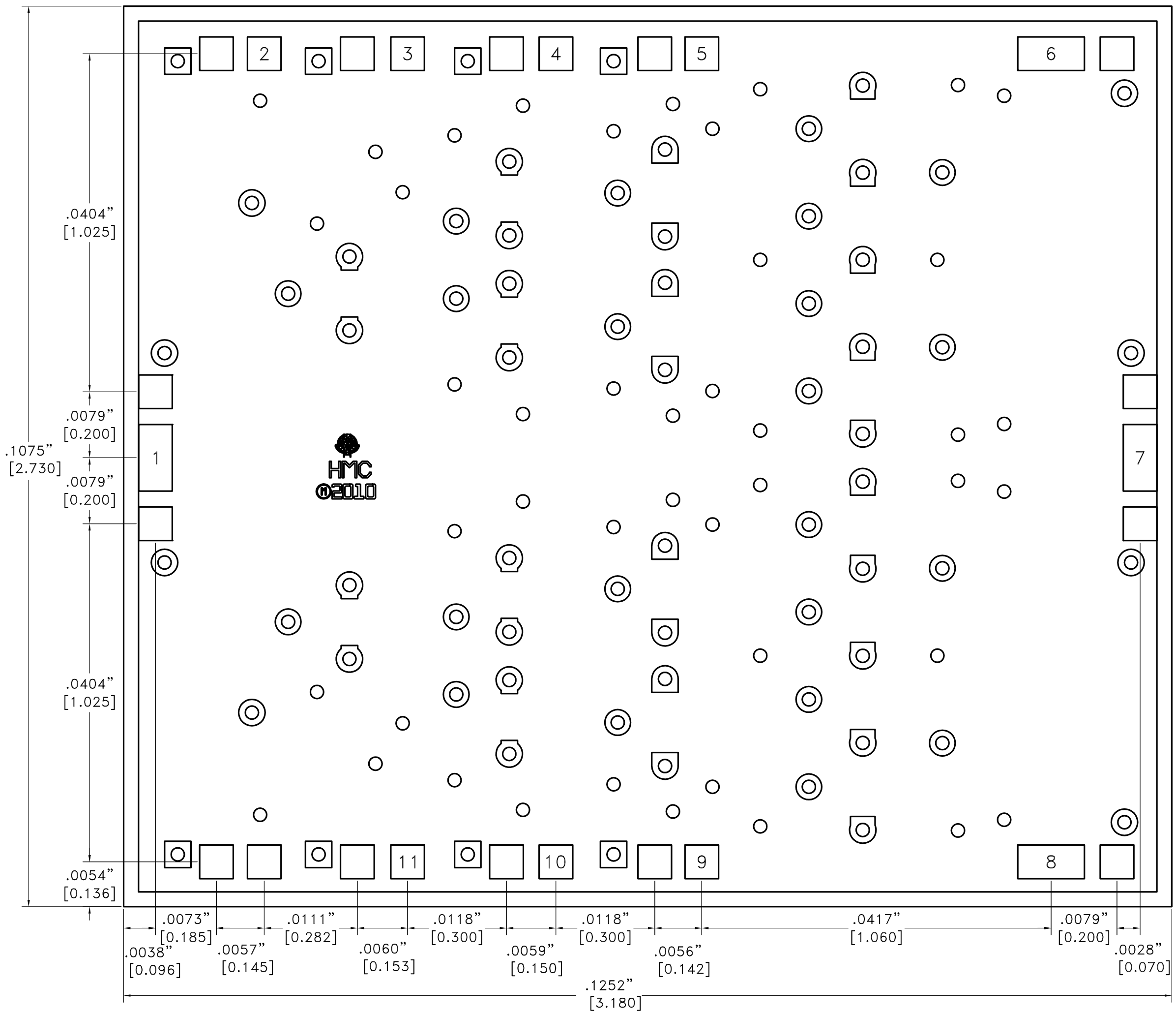
Outline Drawing



Die Packaging Information [1]

Standard	Alternate
GP-1 (Gel Pack)	[2]

[1] Refer to the "Packaging Information" section for die packaging dimensions.
[2] For alternate packaging information contact Hittite Microwave Corporation.

NOTES:
1. ALL DIMENSIONS ARE IN INCHES [MM]
2. DIE THICKNESS IS .004"
3. TYPICAL BOND PAD IS .004" SQUARE
4. BACKSIDE METALLIZATION: GOLD
5. BOND PAD METALLIZATION: GOLD
6. BACKSIDE METAL IS GROUND.
7. CONNECTION NOT REQUIRED FOR UNLABELED BOND PADS.
8. OVERALL DIE SIZE ± 0.002"

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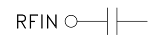
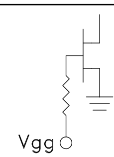
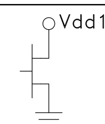
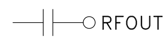
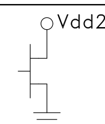
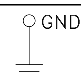
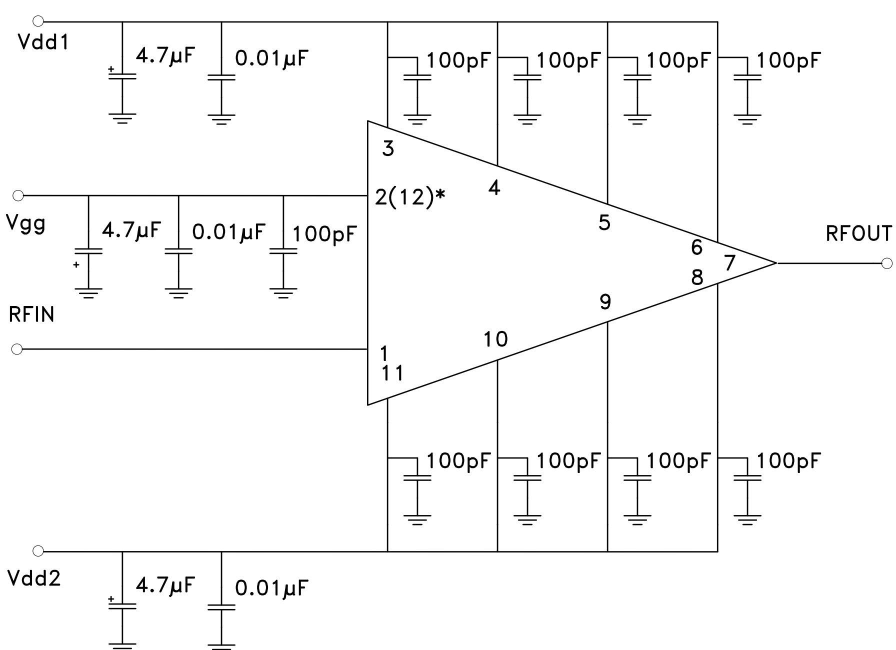
Pad Descriptions

Pad Number	Function	Description	Interface Schematic
1	RFIN	This pad is AC coupled and matched to 50 Ohms over the operating frequency range.	
2, 12	Vgg	Gate control for amplifier. External bypass caps 100 pF, 0.01 µF and 4.7 µF are required. Only one pad connection is required as these two pads are connected on-chip.	
3 - 6	Vdd1	Drain bias voltage for the top half of the amplifier. External bypass capacitors of 100 pF required for each pad, followed by common 0.1 µF and 4.7 µF are capacitors.	
7	RFOUT	This pad is AC coupled and matched to 50 Ohms.	
8 - 11	Vdd2	Drain bias voltage for the lower half of the amplifier. External bypass capacitors of 100 pF required for each pad, followed by common 0.1 µF and 4.7 µF are capacitors.	
Die Bottom	GND	Die bottom must be connected to RF/DC ground.	

Application Circuit



*Vgg may be applied to either pad 2 or pad 12.

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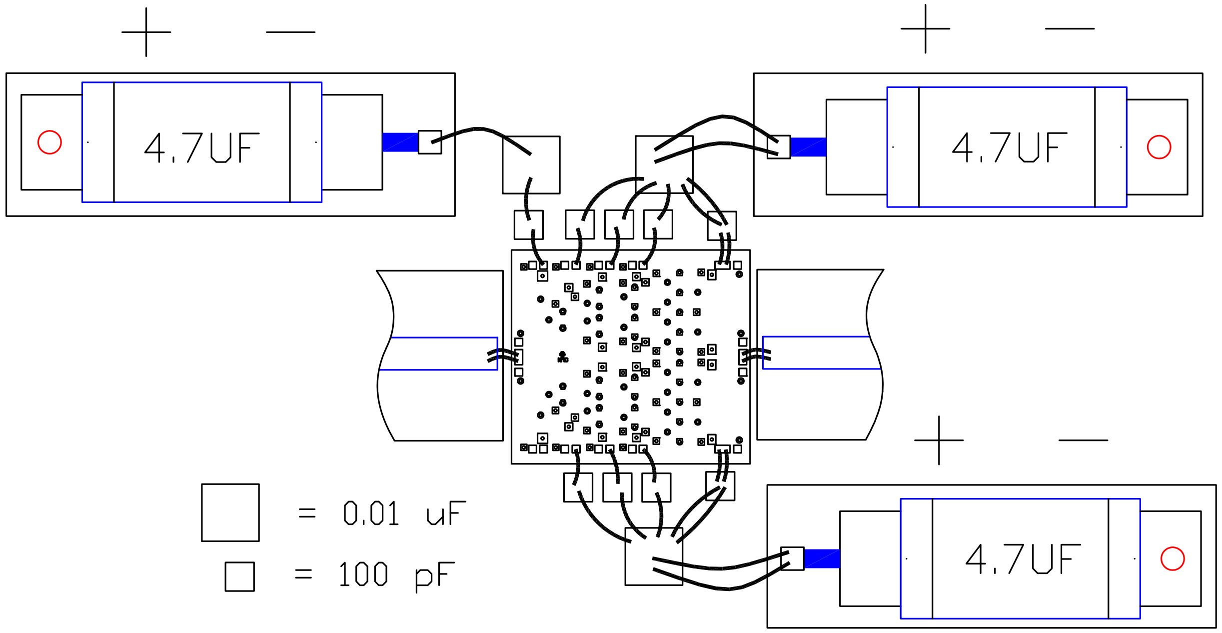
Assembly Diagram [1]



[1] Vgg may be applied to either pad 2 or pad 12.

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Mounting & Bonding Techniques for Millimeterwave GaAs MMICs

The die should be attached directly to the ground plane eutectically or with conductive epoxy (see HMC general Handling, Mounting, Bonding Note).

50 Ohm Microstrip transmission lines on 0.127mm (5 mil) thick alumina thin film substrates are recommended for bringing RF to and from the chip (Figure 1). If 0.254mm (10 mil) thick alumina thin film substrates must be used, the die should be raised 0.150mm (6 mils) so that the surface of the die is coplanar with the surface of the substrate. One way to accomplish this is to attach the 0.102mm (4 mil) thick die to a 0.150mm (6 mil) thick molybdenum heat spreader (moly-tab) which is then attached to the ground plane (Figure 2).

Microstrip substrates should be placed as close to the die as possible in order to minimize bond wire length. Typical die-to-substrate spacing is 0.076mm to 0.152 mm (3 to 6 mils).

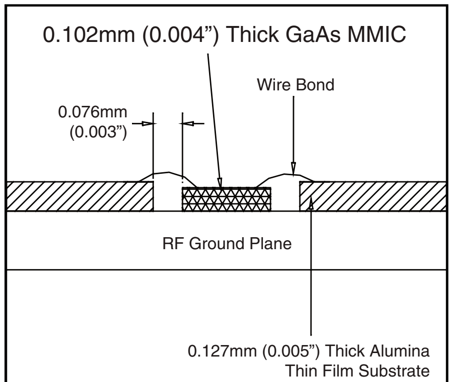

Figure 1.

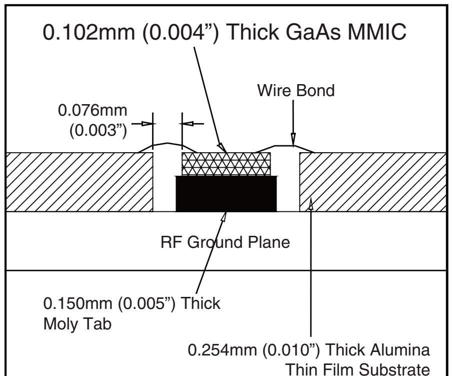

Figure 2.

Handling Precautions

Follow these precautions to avoid permanent damage.

Storage: All bare die are placed in either Waffle or Gel based ESD protective containers, and then sealed in an ESD protective bag for shipment. Once the sealed ESD protective bag has been opened, all die should be stored in a dry nitrogen environment.

Cleanliness: Handle the chips in a clean environment. DO NOT attempt to clean the chip using liquid cleaning systems.

Static Sensitivity: Follow ESD precautions to protect against ESD strikes.

Transients: Suppress instrument and bias supply transients while bias is applied. Use shielded signal and bias cables to minimize inductive pick-up.

General Handling: Handle the chip along the edges with a vacuum collet or with a sharp pair of bent tweezers. The surface of the chip may have fragile air bridges and should not be touched with vacuum collet, tweezers, or fingers.

Mounting

The chip is back-metallized and can be die mounted with AuSn eutectic preforms or with electrically conductive epoxy. The mounting surface should be clean and flat.

Eutectic Die Attach: A 80/20 gold tin preform is recommended with a work surface temperature of 255 °C and a tool temperature of 265 °C. When hot 90/10 nitrogen/hydrogen gas is applied, tool tip temperature should be 290 °C. DO NOT expose the chip to a temperature greater than 320 °C for more than 20 seconds. No more than 3 seconds of scrubbing should be required for attachment.

Epoxy Die Attach: Apply a minimum amount of epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip once it is placed into position. Cure epoxy per the manufacturer’s schedule.

Wire Bonding

RF bonds made with two 1 mil wires are recommended. These bonds should be thermosonically bonded with a force of 40-60 grams. DC bonds of 0.001” (0.025 mm) diameter, thermosonically bonded, are recommended. Ball bonds should be made with a force of 40-50 grams and wedge bonds at 18-22 grams. All bonds should be made with a nominal stage temperature of 150 °C. A minimum amount of ultrasonic energy should be applied to achieve reliable bonds. All bonds should be as short as possible, less than 12 mils (0.31 mm).

Notes:

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